

Amendments to the Specification

Please replace paragraph [0008] with the following amended paragraph:

[0008] Still referring to FIG. 1, a passivation layer 70 is disposed on the gate insulating layer 30 to cover the TFT “T” and has a drain contact hole 71 to expose a portion of the drain electrode ~~[[61]]~~62. The pixel electrode 81 made of a transparent conductive material is formed on the passivation layer 70 and contacts the drain electrode 62 through the drain contact hole 71.